| Memory circuit (8 Months) weekend sessions (sat & sun = 2 hrs).weekdays assignments to the enrolled candidates | |
|---|---|
| Memory Instance Circuit Design | weeks |
| RC,RLC BASICS,SEMICONDUCTOR BASICS | 2 |
| Mos Regions of operation with simulation results analysed | 2 |
| Inverter Fabrication with clear Unerstanding | 1 |
| Unix & scripting | 3 |
| Inverter Design(Sizing) & Characterization | 1 |
| Universal gates Design | 1 |
| High speed Circuit design | 2 |
| Bit cell Design | 4 |
| Sense amplifier design | 4 |
| Decoder Design | 2 |
| Control design | 4 |
| Instance design | 4 |
| - architecture design | 4 |
| 8 months duration | 34 |

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